

Serial No. 08/932,543

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Atty. Dkt. No. 001701.39203

plurality of processing units for executing different operations is not taught or suggested by Pinder.

In Pinder, the "decoded data" may be output from a service reception component (DHCT) 333 in FIG. 3 (or a decoder 240 in FIG. 2B), and supplied to a DHCT secure element (DHCTSE) 627 in FIG. 12 through an interface 1203. According to col. 21, lines 55-57 of Pinder, the microprocessor 1201 executes the code for doing encryption, decryption, and authentication and interpreting EMMs and EMCs. The microprocessor 1201 is specialized hardware for performing RSA encryption and decryption. See, col. 21, lines 34-35, of Pinder.

Pinder does not teach or suggest a plurality of processing units as required in the instant claims. Thus, Pinder does not teach or suggest the invention of the instant claims and withdrawal of the instant rejection is requested.

### CONCLUSION

It is believed that no fee is required for this submission. If any fees are required or if an overpayment is made, the Commissioner is authorized to debit or credit our Deposit Account No. 19-0733, accordingly.

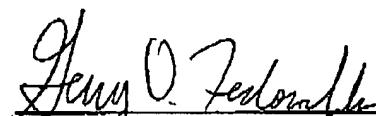
All rejections having been addressed, applicants respectfully submit that the instant application is in condition for allowance, and respectfully solicit prompt notification of the same.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: November 5, 2002

By:

  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

**Yasushi KAWAKURA et al.**

Serial No.: 08/932,543

Filed: September 17, 1997

For: ACCESS CONTROL METHOD AND  
INFORMATION UTILIZATION  
APPARATUS

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Washington, D.C. 20231

**CERTIFICATION OF FACSIMILE TRANSMISSION UNDER 37 C.F.R. § 1.8**

I hereby certify that the attached Amendment After Final Rejection is being facsimile transmitted to the U.S. Patent and Trademark Office on November 5, 2002.

  
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